AMENDMENTS TO SPECIFICATION

Replace the paragraph at page 33, line 16, with the following:

Next, when the reading of the ECC block is finished, the controller 10 copies [[th]] the requested recording data temporarily stored in the fraction data storage memory area 21, in a predetermined position of the read ECC [[bloc]] block data (see FIG. [[5]] 6, (c)).

Replace the paragraph bridging pages 36 and 37 with the following:

Next, in Step 8, the controller 10 determines whether or not not-transferred data exists, which is not yet received from among the host computer requesting recording data. When no not-transferred data exists, Step [[11]] 10 is proceeded on, and it is determined whether or not ECC reading has been completed. There, it is assumed that the not-transferred data exists. In Step 8, when the not-transferred data exists, the controller 10 transfers the data to the recording memory (recording data storage memory area) and stores there.

Replace the paragraph bridging pages 60 and 61 with the following:

As shown in FIG. 15, the controller 10 sets the memory addresses M0 and M4 as a ring start pointer (RS0) and a ring end pointer (RE0) respectively for using the recording data storage memory area in a ring buffering manner in Step [[61]] 62 after the data reproduction processing completion, and determines in Step 63 whether or not a data recording request is given by the host computer.